



AF 2193
MUN

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	DUTTA et al.	Examiner:	Malzahn, D.
Serial No.:	10/005,551	Group Art Unit:	2193
Filed:	November 8, 2001	Docket No.:	US018181 (VLSI.331PA)
Title:	HIGH-SPEED COMPUTATION IN ARITHMETIC LOGIC CIRCUIT		

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence and the papers, as described hereinabove, are being deposited in the United States Postal Service, as first class mail, in an envelope addressed to: Mail Stop Final, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 10, 2005.

By:

Kelly Waltgney

OFFICE ACTION RESPONSE AND AMENDMENT

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the final Office Action dated October 17, 2005, please consider the following remarks.

The pending claims begin on page 2 and remarks begin on page 6.